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## BIB DATA SHEET

CONFIRMATION NO. 7818

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**APPLICANTS**

Yukio Arima, Osaka, JAPAN;

**\*\* CONTINUING DATA \*\*\*\*\***

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**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2004-112786 04/07/2004

**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***

04/25/2007

Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No				
Verified and Acknowledged	/SAM WADIE RIZK/ Examiner's Signature	Initials	JAPAN	11	14

**ADDRESS**

MCDERMOTT WILL & EMERY LLP  
 600 13TH STREET, NW  
 WASHINGTON, DC 20005-3096  
 UNITED STATES

**TITLE**

Path memory circuit

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